

In re Patent Application of
PAU ET AL.
Serial No. **Not Yet Assigned**
Filed: **Herewith**

A1
CONCL.
RULE 35
1.126 36. A device according to Claim 32 further comprising a bit rate control circuit connected to said encoder for setting quantizing of the intermediate sequence of data by said quantizer.

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~~37.~~ A device according to Claim 32 wherein said third circuit comprises a multiplexer connected to outputs of said first circuit, said second circuit and said encoder.

REMARKS

It is believed that all of the claims are patentable over the prior art. Accordingly, after the Examiner completes a thorough examination and finds the claims patentable, a Notice of Allowance is respectfully requested in due course. Should the Examiner determine any minor informalities that need to be addressed, he is encouraged to contact the undersigned attorney at the telephone number below.

Respectfully submitted,

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